

AMENDMENTS TO THE SPECIFICATION

Please amend paragraph [0047] of the specification as follows:

[0047] A processor based system, such as a computer system, for example generally comprises, in addition to a CMOS imager 642 input device, a central processing unit (CPU) 644, for example, a microprocessor, that communicates with one or more input/output (I/O) devices 646 over a bus 652. The CMOS image sensor 642 also communicates with the processor system over bus 652 or over other conventional communication path. The computer system 600 also includes random access memory (RAM) 648, and, in the case of a computer system may include peripheral devices such as a floppy disk drive 654, and a compact disk (CD) ROM drive 656 or a flash memory card 657 which also communicate with CPU 644 over the bus 652. It may also be desirable to integrate the processor ~~[[654]]~~ 644, CMOS image device 642 and memory 648 on a single IC chip.